

**IN THE UNITED STATES PATENT AND TRADEMARK OFFICE**

Applicant: **Chi-Shen LEE, Yong-Fu WU,  
Chi-Lin CHEN, Cheng-Chung  
CHEN**

Filed: **9/30/2003**

Appl. No.: **10/673,326**

Examiner: **MCPHERSON, JOHN**

Conf. No.: **2258**

Art Unit: **1756**

Title: **METHOD OF MANUFACTURING A TFT ARRAY PANEL FOR A LCD**

Date: **March 23, 2007**

Mail Stop ISSUE FEE  
Commissioner for Patents  
P.O. Box 1450  
Alexandria, VA 22313-1450

**FEE(S) TRANSMITTAL AND FORMAL DRAWINGS**

Sir:

Attached please find:

- 1) Form PTOL-85 Part B – Fee(s) Transmittal (**1 page**);
- 2) Four (4) sheets of corrected drawings required by the Examiner in the Notice of Allowance mailed on January 24, 2007, wherein the replacement sheets replace Figs. 1c-1j.

The Office is authorized to charge any additional fees or underpayment of fees as may be required to deposit account **502447**.

Respectfully submitted,

/Nelson A. Quintero/

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